

# Radiens Release Notes

Updated November 25, 2024

## Radiens Update

A new version of Allego has been released.

<https://www.neuronexus.com/radiens-allego-download-page/>

### Allego 3.9.0:

Features/Changes:

- Split ports recording mode (i.e., control recording on each port separately)
- Enable saving of spikes data alongside, or in addition to, continuous data (record GPIOs in xdat file along side in spikes-only mode)
- Split recording of DIN channels per port in split ports mode
  - port:DIN channel mapping is the same as port:DOUT channel mapping, as follows:
    - DIN/DOUT ntvIdx 0 (first) is not used, as DOUT 0 is reserved for system-wide streaming state, which is not relevant for split ports recording
    - Port A maps to DIN/DOUT 1, Port B to DIN/DOUT 2, etc
      - Except for XDAQ Core, because DIN/DOUT 1 is not physically available on the case. So Port A maps to DIN/DOUT 2, Port B maps to DIN/DOUT 3
- Update DIN triggering to also use the above mapping in split ports mode
- Enable user to control the polarity of DOUT Gated mode signaling
- Add option to load device files from local dir in Electrodes Tab
- Spike sorting (scraping + clustering) can optionally have clustering disabled to lighten the load, specifically in high channel count situations

Fixes:

- Stability improvements
- Efficiency improvements for spike scraping